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Atty. Docket No.: INFJR-0005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jens Barrenscheen, et al

Application No.: 10/727,102

Group No.:2182

Filing Date: December 2, 2003

Examiner: Jeffrey A. Gaffin

For: "Arrangement Comprising A First Semiconductor Chip And A Second
Semiconductor Chip Connected Thereto"

Date: April 27, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

1. More than twelve months have passed since the filing of this application on December 2, 2003. No communication has been received from the Patent and Trademark Office indicating action on this application.
2. Kindly advise the undersigned of the present status of this application.

SIGNATURE OF PRACTITIONER

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